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QORAI CORP Announces Q4 Nexus, a Patent-Pending Quad Logic Computing Platform Advancing from Software Model to FPGA Testing

Q4 Nexus integrates QDOS, BTRC symbolic coding, Spatial Weave Memory, FPGA validation, and future photonic silicon research pathways for adaptive intelligence systems

ORLANDO, Fla. — October 7, 2026 — QORAI CORP, an Orlando-based deep-tech computing company, today announced the continued development of **Q4 Nexus**, its patent-pending Quad Logic computing platform designed to move beyond conventional binary-only computing toward a four-state symbolic architecture for adaptive intelligence.

Q4 Nexus is being developed around QORAI's **BTRC coding model**, which organizes computation and memory through four functional symbolic states: **Base, Time, Regenerate, and Context**. Together, these states are intended to help future computing systems preserve not only data, but also its origin, sequence, relationships, and transformation history.

The company reports that its engineering team has advanced Q4 Nexus from software-defined architecture into early FPGA board testing, validating key elements of the **QDOS developer environment**, Q4 instruction concepts, and Spatial Weave Memory operations in a staged hardware pathway.

“AI has advanced rapidly, but the architecture beneath it still carries deep limitations in memory movement, context loss, cloud dependence, and energy demand,” said **Robby Joshi, AIA, Founder and CEO of QORAI CORP**. “QORAI is not simply building another AI application. We are developing a new computing foundation designed for adaptive intelligence — one that can remember, trace, connect, regenerate, and evolve.”

From Binary Logic to Quad Logic

Modern computing remains built primarily around binary logic, linear memory structures, and data movement between processors, memory, storage, and accelerators. While this architecture has powered decades of

innovation, QORAI believes the next generation of AI, robotics, edge systems, and intelligent devices will require computing systems that can preserve context, track change, and operate closer to the user.

Q4 Nexus is designed as a staged four-state computing platform that includes:

- **QDOS**, a developer-facing shell for loading, running, tracing, and diagnosing Q4 programs
- **Q4-ISA**, a native Quad Logic instruction model based on three-symbol BTRC triads
- **Spatial Weave Memory**, a memory architecture designed around Base, Time, Context, and Regeneration
- **Q4 Core execution**, a pathway for four-state symbolic processing
- **FPGA validation**, bridging software models and future hardware acceleration
- **Photonic and silicon research pathways**, exploring future methods for reducing electrical noise and improving state transport

The company describes the current FPGA testing stage as an important step in moving Q4 Nexus from theoretical architecture into practical engineering validation.

Spatial Weave Memory and BTRC Coding

At the center of Q4 Nexus is **Spatial Weave Memory**, QORAI's adaptive memory architecture. Unlike conventional memory systems that store data primarily by address, Spatial Weave Memory is designed to organize information through logical coordinates and transformation history.

In QORAI's preferred model:

Base + Time + Context = Memory Location
Regeneration = Memory Transformation

This approach is intended to allow each **Spatial Weave Cell Unit**, or SCU, to preserve memory payload, origin, time sequence, contextual relationships, prior states, and regenerative history.

For AI systems, this could support more persistent contextual memory, traceable knowledge evolution, reduced reliance on fragmented external memory layers, and improved local intelligence over time.

Potential Benefits for the AI Industry

QORAI believes Q4 Nexus may provide a new architectural direction for several AI infrastructure challenges, including:

- Reducing unnecessary data movement
- Supporting local and edge AI systems
- Improving memory traceability and provenance
- Preserving context across sessions and workloads
- Supporting adaptive memory for robotics and autonomous systems
- Creating a software-to-hardware path for future intelligent computing platforms

The company emphasizes that Q4 Nexus is being developed in stages and that performance claims will require continued testing, benchmarking, and independent validation.

“Binary computing remains foundational,” said Joshi. “Q4 Nexus is not about dismissing the past. It is about extending the computing stack with new symbolic, contextual, and regenerative layers that may better support the age of adaptive intelligence.”

Future Photonic Silicon Research

In addition to FPGA and mixed-signal research, QORAI is also exploring future **photonic silicon design pathways** for Q4 state transport. The company is evaluating how BTRC states may eventually be represented or transferred through optical channels, wavelengths, pulses, or other light-based mechanisms to help reduce electrical noise and interconnect pressure in selected computing pathways.

QORAI describes this photonic direction as a long-term research area, not an immediate commercial claim.

Patents Filed and Pending

QORAI CORP has filed patent applications covering its broader Q4 computing platform and Spatial Weave Memory system. The company’s patent-pending architecture includes elements related to multi-state computing, BTRC symbolic coding, Spatial Weave Memory, Q4 instruction structures, regenerative memory, and staged hardware pathways.

QORAI’s mission is to build computing systems that can remember, trace, connect, regenerate, and adapt.

About QORAI CORP

QORAI CORP is an Orlando-based deep-tech computing company developing **Q4 Nexus**, a patent-pending computing platform for adaptive intelligence. The company’s work includes Quad Logic computing, Spatial Weave Memory, QDOS, FPGA validation, Guardian Kernel safety concepts, and future hardware research pathways.

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Forward-Looking Statement: Statements regarding QORAI’s technology roadmap, Guardian Kernel, Spatial Weave Memory, Q4 hardware research, commercialization plans, potential benefits, and industry impact are forward-looking and subject to continued research, engineering validation, testing, market adoption, regulatory review, investor financing, and business execution. No future performance result, product release, safety outcome, commercial deployment, valuation, or industry adoption is guaranteed.

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